JUN. 25. 2004 4:25PM

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MORRISON & FOERSTER LLP

Attorneys at Law 425 Market Street San Francisco, California 94105-2482 Telephone: (415) 268-7000

Facsimile: (415) 268-7522

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FROM: Robert E. Scheid (Reg. No. 42,126) **DATE:** June 25, 2004

12 Number of pages with cover page:

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Contents of this Transmission:

Attv Docket No.:306812002601

Inventor: David M. LEWIS et al.

Application No.: 10/623,709

Filing Date: July 21, 2003

Title:

ROUTING ARCHITECTURE FOR A PROGRAMMABLE LOGIC DEVICE

Documents Filed: Transmittal (1 page)

Request for Corrected Filing Receipt (2 pages)

Copy of Marked Up Updated Filing Receipt (2 pages)

Copy of Assignment documents from parent application (6 pages)

Sender's Initials:

RES4/jxh3

Date

June 25, 2004

PTC/SB/21 (08-08)
Approved for use through 07/31/2008. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information untess it displays a valid OMB control number. Application Number 10/623,709 Filing Date TRANSMITTAL July 21, 2003 First Named Inventor **FORM** David LEWIS Art Unit 2819 (to be used for all correspondence after initial filling) Examiner Name Not Yet Assigned Attorney Docket Number Total Number of Pages in This Submission 11 306812002601 ENCLOSURES (Check all that apply) After Allowance Communication Fee Transmittal Form Drawing(s) Appeal Communication to Board of Fee Attached Licensing-related Papers Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Amendment/Reply Petition Petition to Convert to a After Final Proprietary Information Provisional Application Power of Attorney, Revocation Affidavits/declaration(s) Status Letter Change of Correspondence Address Other Enclosure(s) (please X Other Divov. Extension of Time Request Terminal Disclaimer 1. Request For Corrected Filing Express Abandonment Request Request for Refund Receipt (2 pages) 2. Copy of Marked Up Updated Information Disclosure Statement CD, Number of CD(s) Filing Receipt (2 pages)
3. Copy of Assignment documents Certified Copy of Priority from parent application (6 pages) Document(s) Remarks Response to Missing Parts/ Incomplete Application Facsimile Return Receipt Cover is Uncounted Page of this Response to Missing Parts Transmission under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm MORRISON & FOERSTER LLP (Customer No. 20872) Robert E. Scheid - 42,126 individual name Signature Date June 25, 2004

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Dated: June 25, 2004

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Docket No.: 306812002601

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

David LEWIS et al.

Application No.: 10/623,709

Confirmation No.: 5382

Filed: July 21, 2003

Art Unit: 2819

For: ROUTING ARCHITECTURE FOR A PROGRAMMABLE LOGIC DEVICE

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in the field entitled "Assignment For Published Patent Application". This application is a continuation application, as cited in first paragraph of said application, and the parent application, 10/140,287 filed May 6, 2002, U.S. Patent 6,630,842, has been assigned to Altera Corporation. Please remove "Donnelly Corporation, Holland, MI;" from this Updated Filing Receipt and insert --Altera Corporation, San Jose, CA--.

Application No.: 10/623,709

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Docket No.: 306812002601

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: June 25, 2004

Respectfully submitted

Robert E. Scheid

Registration No.: 42,126

MORRISON & FOERSTER LLP

425 Market Street

San Francisco, California 94105

(415) 268-6369







United States Patent and Trademark Office

United States DEPARTMENT OF COMMERCE United States Patent and Trademert Office Address COMMISSIONER FOR PATENTS RO. Box 1950 Alexandra Vegina 22313-1450

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/823,709	07/21/2003	2819	900	306812002601	8	20	2

Michael Mauriel Morrison & Foerster LLP 425 Market Street San Francisco, CA 94105-2482

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. 1*8* CONFIRMATION NO. 5382

Date Mailed: 05/05/2004

MORRISON & FOERSTER, LLP

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

David M. Lewis, Toronto, CANADA;
Paul Leventis, Toronto, CANADA;
Andy L. Lee, San Jose, CA;
Brian D. Johnson, Sunnyvale, CA;
Richard Cliff, Los Altos, CA;
Srinivas T. Reddy, Fremont, CA;
Christopher F. Lane, San Jose, CA;
Cameron R. McClintock, Mountain View, CA;
Vaughn Betz, Toronto, CANADA;
Chris Wysocki, Toronto, CANADA;
Alexander R. Marquardt, Toronto, CANADA;

Assignment For Published Patent Application

-- Altera Corporation, Holland, MI: -- Altera Corporation, 45

Domestic Priority data as claimed by applicant

This application is a CON of 10/140,287 05/06/2002 PAT 6,630,842 which claims benefit of 60/289,178 05/06/2001

Foreign Applications

If Required, Foreign Filing License Granted: 10/22/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request; No



Title

Routing architecture for a programmable logic device

Preliminary Class

326

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